IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s)	:	Joseph ROHLMAN et al.
Serial No.	:	Not assigned
Filing Date	:	Herewith
For	:	MICRO-INSTRUCTION QUEUE FOR A MICROPROCESSOR INSTRUCTION PIPELINE
Group Art Unit	:	Not assigned
Examiner	:	Not assigned

Box Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Please amend the above-identified application as set forth below.

IN THE SPECIFICATION:

Please amend the specification as follows.

On page 1, before line 3, please add the following new paragraph:

--**RELATED APPLICATIONS**

This application is a continuation of U.S. Patent Application No. 09/223,219, filed on December 30, 1998, which is expressly incorporated herein by reference in its entirety.--.

IN THE CLAIMS:

Please cancel claims 1-27, without prejudice.
Please add new claims 28-42 as follows:

28. (New) An instruction pipeline in a microprocessor, comprising:

a plurality of pipeline units, each of the pipeline units configured to process instructions, at least one of the plurality of pipeline units configured to receive the instructions from another of the pipeline units and store the instructions, wherein the